

IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicant: Ling-Wei KE Conf.: 4501
Appl. No.: 10/190,559 Group:
Filed: July 9, 2002 Examiner:
For: Emitter Coupled Logic Circuit with a
Data Reload Function

L E T T E R

Assistant Commissioner for Patents
Washington, DC 20231

September 18, 2002

Sir:

Under the provisions of 35 U.S.C. § 119 and 37 C.F.R. § 1.55(a), the applicant(s) hereby claim(s) the right of priority based on the following application(s):

| <u>Country</u> | <u>Application No.</u> | <u>Filed</u> |
|----------------|------------------------|---------------|
| TAIWAN | 090117157 | July 12, 2001 |

A certified copy of the above-noted application(s) is (are) attached hereto.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fee required under 37 C.F.R. §§ 1.16 or 1.17; particularly, extension of time fees.

Respectfully submitted,

BIRCH, STEWART, KOLASCH & BIRCH, LLP

By 
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3722-0126P

Attachment